

KC96 Board Specification Sheet

Model No.: KC96 (Part No.: C96--556N00--)

Main Features:

- ◎ Auto detection of separate Sync. Signal and composite Sync.Signal of VGA
- ◎ Support VGA analog standard input signal (15-pin D-Sub connectors)
- ◎ Support vertical refresh rate up to 75Hz for VGA~SXGA+ (1400X1050) and 60Hz for higher resolution of VESA Standard Timing
- ◎ Supply single pixel(24bits) or dual pixel(48bits)digital RGB output
- ◎ Compatible with VGA up to FHD (1920x1080) LCD panel
- ◎ Operating temperature : 0 °C to +50 °C
- ◎ Storage temperature : -40 °C to +70 °C
- ◎ Operating humidity : 0 ~ 80% (Non- condensing)
- ◎ Storage humidity : 0 ~ 90%
- ◎ Support RS232 (Optional)
- ◎ Support HDMI input
- ◎ Support DVI input
- ◎ Audio (Phone Jack)
- ◎ RoHS compliant

Electrical Specification

Power supply :	
Input for A/D board	12V DC 3A
Output for eDP Panel	3.3V / 5V
Output for other board	5V
Power consumption (Panel / Inverter not included) :	
Power saving	<0.15W
Power off	<0.12W

Support Timing Table

Mode	Resolution	H Frequency(KHz)	V Frequency(Hz)
1	640*350@70	31.469	70.087
2	640*480@60	31.469	59.94
3	640*480@72	37.861	72.809
4	640*480@75	37.5	75
5	720*400@70	31.469	70.087
6	800*600@56	35.156	56.25
7	800*600@60	37.879	60.317
8	800*600@72	48.077	72.188
9	800*600@75	46.875	75
10	1024*768@60	48.363	60.004
11	1024*768@70	56.476	70.069
12	1024*768@72	58.038	71.918
13	1024*768@75	60.023	75.029

14	1280*70@50	36.91	49.82
15	1280*720@60	44.62	59.81
16	1280*1024@60	63.98	60
17	1280*1024@70	74.882	69.853
18	1280*1024@75	79.976	75.025
19	1360*768@60	47.72	59.799
20	1440*900@60	55.935	59.887
21	1680*1050@60	65.3	60
22	1920*1080@50	56.25	50
23	1920*1080@60	66.6	60

Connector Description

Connector List

Connector	Function	Note
J6	VGA input connector	2.0mm. 14Pin. 90°
J5	HDMI input connector	2.0mm. 13Pin. 90°
J2	Power input connector	2.0mm. 4Pin. 90°
J10	LCD Panel for eDP	1.25mm. 30Pin.90°
J9	Key pad connector	2.0mm. 10Pin.90°
J1	Inverter connector	2.0mm .8Pin.90°
J4	Micro SW connector	2.0mm. 2Pin. 90°
J3	5V output connector	2.0mm. 2Pin. 90°

Pin Definition for Connector

J1: Inverter Connector (2.0mm 8 Pin SMD)

Pin	Function	Note
1,2,8	12V Input	
3,5,7	GND	
4	Brightness control	0V-5V Analog or PWM
6	ON/OFF Control	5V ON;0V OFF

J9: Key Board Connector (2.0 mm 10Pin SMD)

Pin	Function	Note
1	LED-G	
2	N.C.	
3	LED-R	
4	N.C.	
5	AUTO	
6	MENU	
7	SEL+(RIGHT)	
8	SEL-(LEFT)	
9	GND	
10	POWER	

J2: Power Input Connector (2.0 mm 4Pin SMD)

Pin	Description	Note
1	DC	
2	DC	
3	GND	
4	GND	

J3: 5V Output (2.0 mm 2Pin SMD)

Pin	Description	Note
1	GND	
2	5V	

J4: Micro SW Connector (2.0 mm 2Pin SMD)

Pin	Description	Note
1	GND	
2	SW	

J6:VGA Input Connector (2.0 mm 14 Pin SMD)

Pin	Description	Note
1	RED	
2	GND	
3	GREEN	
4	GND	
5	BLUE	
6	GND	
7	H. SYNC	
8	V. SYNC	
9	GND	
10	DDC SDA	
11	DDC SCL	
12	VGA_5V	
13	DET_VGA	
14	NC	

J5:DVI Input Connector (2.0 mm 13 Pin SMD)

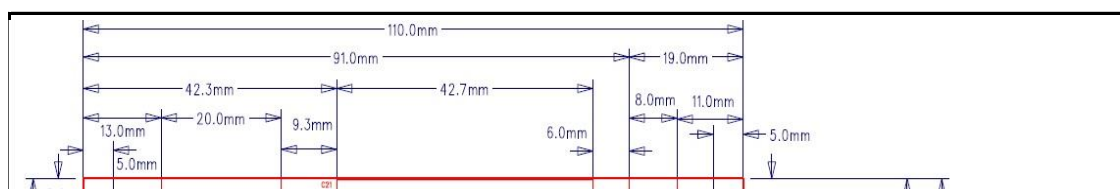
Pin	Description	Note
1	CLK-	
2	CLK+	
3	DATA0-	
4	DATA0+	
5	DATA1-	
6	DATA1+	
7	DATA2-	
8	DATA2+	

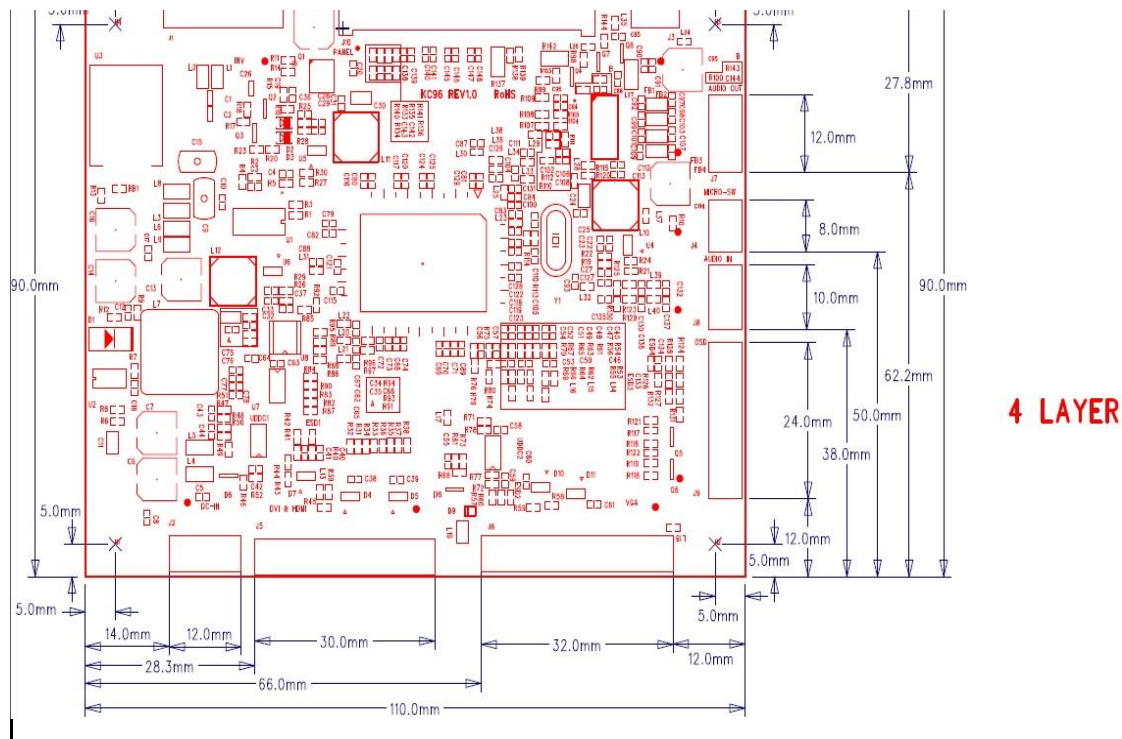
9	GND	
10	DVI_5V	
11	DDC2 SDA	
12	DDC2 SCL	
13	DET_DVI	

J10: Panel Connector (1.25 mm 30 Pin SMD)

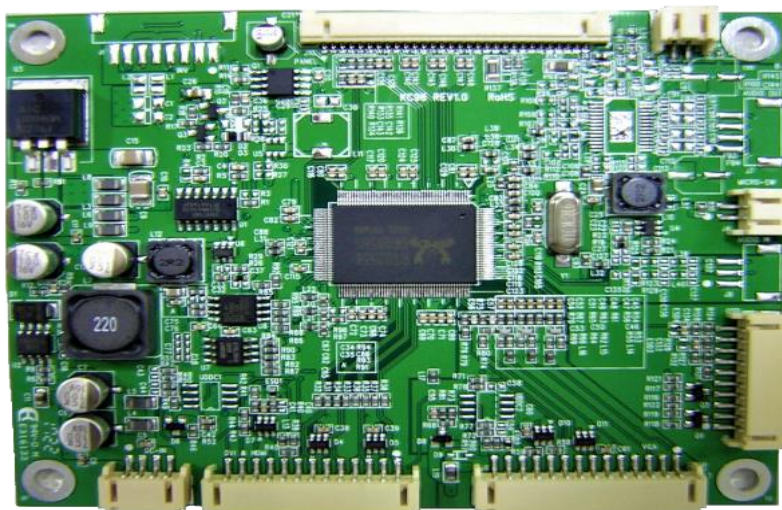
Pin	Name	Note
1	NC	No contact
2	NC	No contact
3	GND	Ground
4	TXAUXN R	Complement signal Auxiliary Channel
5	TXAUXP R	True signal Auxiliary Channel
6	GND	Ground
7	TX0N R	Complement signal link lane0
8	TX0P R	True signal link lane0
9	GND	Ground
10	TX1N R	Complement signal link lane1
11	TX1P R	True signal link lane1
12	GND	Ground
13	TX2N R	Complement signal link lane2
14	TX2P R	True signal link lane2
15	GND	Ground
16	TX3N R	Complement signal link lane3
17	TX3P R	True signal link lane3
18	GND	Ground
19	3.3V or 5V	+3.3V or +5V Power supply
20	3.3V or 5V	+3.3V or +5V Power supply
21	DP_HPD	HPD Signal in
22	BKLT_EN	Backlight enable
23	BKLT_ADJ	Backlight Adjustment
24	GND	Ground
25	GND	Ground
26	12V	+12V Power supply
27	12V	+12V Power supply
28	12V	+12V Power supply
29	12V	+12V Power supply
30	NC	No contact

Board Dimension : 110(W) mm x 90 (H)mm





Board Photo:



※Specifications and product design are subject to change without notice.

